ABSTRACT

Desired signal power measured at a desired signal power measuring circuit 105 is averaged over a plurality of slots by an averaging circuit 106 to

5   reduce a power error in desired signal power of each slot. An SIR measuring circuit 108 calculates SIR(n)of each slot from the average value of desired signal power in the plurality of slots and the measured value of interference signal power of each

10   slot, and a TPC generating circuit 109 makes a comparison between SIR(n) of each slot and a threshold value, and generates transmission power control information. This makes it possible to control transmission power for each slot with high

15   accuracy in asymmetrical communication.